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The International Conference for High Performance Computing, Networking, Storage and Analysis
Austin, TX
November 15 - 20, 2015

10th parallel data storage workshop (PDSW'15) call for papers

Monday, November 16, 2015
9:00am - 5:30pm
Held in conjunction with SC15
Co-Chairs:
Jay Lofstead, Sandia National Laboratories
Ali R. Butt, Virginia Tech

Workshop Abstract:

Peta and exascale computing infrastructures make unprecedented demands on storage capacity, performance, concurrency, reliability, availability, and manageability. This one day workshop focuses on the data storage and management problems and emerging solutions found in peta and exascale scientific computing environments, with special attention to issues in which community collaboration can be crucial for problem identification, workload capture, solution interoperability, standards with community buy-in, and shared tools. Addressing storage media ranging from tape, HDD, and SSD, to emerging storage devices like NVRAM, the workshop seeks contributions on relevant topics, including but not limited to:

- performance and benchmarking
- failure tolerance problems and solutions
- APIs for high performance features
- parallel file systems
- high bandwidth storage architectures
- support for high velocity or complex data
- metadata intensive workloads

- autonomics for HPC storage
- virtualization for storage systems
- archival storage advances
- resource management innovations
- incorporation of emerging storage technologies
- workload study from production systems, and
- programmability of storage systems

Paper Submissions: http://www.pdsw.org/

Paper (in pdf format) due Wednesday, Sept. 16, 2015, 9:00 pm PDT
Notification: Wednesday, Sept. 30, 2015
Camera ready due: Friday, Oct. 9, 2015
Slides due: Sunday, Nov. 15, 2015, 5:00 pm PDT, BEFORE the workshop

Paper Submission Details:

The Parallel Data Storage Workshop holds a peer reviewed competitive process for selecting short papers. Submit a not previously published short paper of up to 5 pages, not less than 10 point font and not including references, in a PDF file as instructed on the workshop web site. Submitted papers will be reviewed under the supervision of the workshop program committee. Submissions should indicate authors and affiliations. Final papers must not be longer than 5 pages (excluding references). Selected papers and associated talk slides will be made available on the workshop web site; the papers will also be published in the digital library of the IEEE or ACM.

Work-in-progress (WIP) Submissions: http://www.pdsw.org/

There will also be a WIP session at the workshop, where presenters give 5-minute brief talks on their on-going work, with fresh problems/solutions, but may not be mature or complete yet for paper submission. A 1-page abstract is required as instructed on the workshop web site.

WIP Submission Deadline: Sunday, Nov. 1, 2015
WIP Notification: Friday, Nov. 6, 2015

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PDSW '15 is being held in cooperation with SIGHPC

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